

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s) Chyan

Case 15-6-9

Serial No. 09/648164

Group Art Unit 2826

Filing Date August 25, 2000

Examiner Dickey

Title Architecture For Circuit Connection Of A Vertical Transistor

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D. C. 20231


SIR:

CHANGE OF CORRESPONDENCE ADDRESS

In accordance with the Manual of Patent Examining Procedures, Section 601.03, please change the mailing address for all correspondence in the above-identified application to:

Agere Systems Inc.
4 Connell Drive, Room 4U-533C
Berkeley Heights, NJ 07922-2747 USA
(908) 508-8513 Fax: (908) 508-8326

Respectfully,


Ferdinand M. Romano
Reg. No. 32752
Phone 407-371-3250

Date: August 19, 2003

Agere Systems Inc.
4 Connell Drive, Room 4U-533C
Berkeley Heights, NJ 07922-2747 USA